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[56] **References Cited**

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[57] **ABSTRACT**

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The program circuit according to the present invention can apply a program voltage to the only memory cells which are not programmed during a re-programming operation, thus, the present invention can prevent a lowering of reliability of the memory cell due to a continued supply of a program bias voltage.

**4 Claims, 1 Drawing Sheet**

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[58] **Field of Search** ..... 365/189.07, 201,  
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